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August 2013

BD135 / 137 / 139 NPN Epitaxial Silicon Transistor

Features

• Complement to BD136, BD138 and BD140 respectively

Applications

· Medium Power Linear and Switching



Ordering Information

Part Number	Marking	Package	Packing Method
BD13516S	BD135-16		Bulk
BD1356STU	BD135-6		
BD13510STU	BD135-10		
BD13516STU	BD135-16		Rail
BD13716STU	BD137-16		
BD13710STU	BD137-10	TO-126 3L	
BD13716S	BD137-16	10-120 3L	Bulk
BD13916STU	BD139-16		Rail
BD13910S	BD139-10		Bulk
BD13916S	BD139-16		Bulk
BD1396STU	BD139-6		Rail
BD13910STU	BD139-10		IXall

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Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter		Parameter		Value	Units
		BD135	45			
V_{CBO}	Collector-Base Voltage	BD137	60	V		
		BD139	80			
\/		BD135	45			
V _{CEO} Collector-Emitter	Collector-Emitter Voltage	BD137	60	V		
		BD139	80			
V _{EBO}	Emitter-Base Voltage		5	V		
I _C	Collector Current (DC)		1.5	А		
I _{CP}	Collector Current (Pulse)		3.0	А		
I _B	Base Current		0.5	А		
В	ovice Dissipation	T _C = 25°C	12.5	W		
P _C Device Dissipati	Device Dissipation	T _A = 25°C	1.25	W		
TJ	Junction Temperature		150	°C		
T _{STG}	Storage Temperature		- 55 to +150	°C		

Electrical Characteristics

Values are at T_C = 25°C unless otherwise noted.

Symbol	Parameter		Test Condition	Min.	Тур.	Max.	Units
	Collector Emitter Sustaining	BD135		45			
V _{CEO} (sus)	Collector-Emitter Sustaining Voltage	BD137	$I_C = 30 \text{ mA}, I_B = 0$	60			V
	voltage	BD139		80			
I _{CBO}	Collector Cut-off Current		$V_{CB} = 30 \text{ V}, I_{E} = 0$			0.1	μΑ
I _{EBO}	Emitter Cut-off Current		$V_{EB} = 5 \text{ V}, I_{C} = 0$			10	μΑ
h _{FE1}			$V_{CE} = 2 \text{ V}, I_{C} = 5 \text{ mA}$	25			
h _{FE2}	DC Current Gain		$V_{CE} = 2 \text{ V}, I_{C} = 0.5 \text{ A}$	25			
h _{FE3}		$V_{CE} = 2 \text{ V}, I_{C} = 150 \text{ mA}$	40		250		
V _{CE} (sat)	Collector-Emitter Saturation Voltage		$I_C = 500 \text{ mA}, I_B = 50 \text{ mA}$			0.5	V
V _{BE} (on)	Base-Emitter On Voltage		$V_{CE} = 2 \text{ V}, I_{C} = 0.5 \text{ A}$			1	V

h_{FE} Classification

Classification	6	10	16	
h _{FE3}	40 ~ 100	63 ~ 160	100 ~ 250	

Typical Performance Characteristics

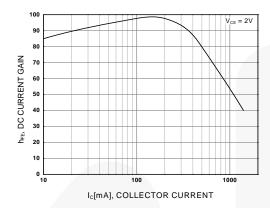


Figure 1. DC current Gain

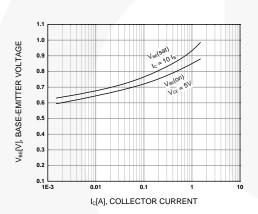


Figure 3. Base-Emitter Voltage

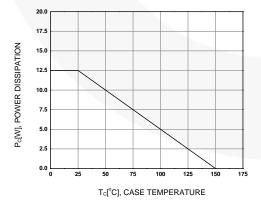


Figure 5. Power Derating

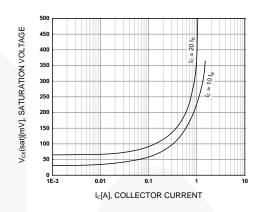


Figure 2. Collector-Emitter Saturation Voltage

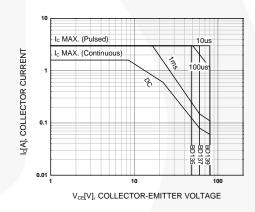
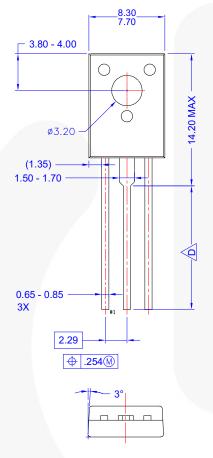
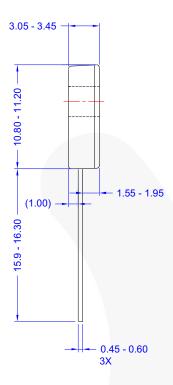


Figure 4. Safe Operating Area

Physical Dimensions

TO-126 3L





PRODUCTION CODE	TERMINAL LENGTH "D"
TSSTU	3.45-4.05
TSTU	2.36-2.96
NONE (STD LENGTH)	12.76-13.36

NOTES:

- A) THIS PACKAGE DOES NOT COMPLY TO ANY CURENT PACKAGING STANDARD.
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH,
- AND TIE BAR EXTRUSIONS.
- D) FOR TERMINAL LENGTH SEE TABLE
- E) DRAWING FILE NAME AND REVISION: MKT-TO126AArev1

Figure 6. TO-126 (SOT-32) UNIFIED DRAWING (TSTU, TSSTU, STANDARD)

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Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings: http://www.fairchildsemi.com/dwg/TO/TO126AA.pdf.

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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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