

Switching amplifier - DEK-TR/INV - 2964319

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Inverter module. The inverter module inverts the signals of ground switching n-p-n transistor outputs into plus switching p-n-p outputs, and vice versa.

Product Features

- ✓ The DEK-TR/INV inverter module inverts the signals of ground switching NPN transistor outputs into positive switching PNP outputs and vice versa



Key commercial data

package_quantity	10
GTIN	4017918176068

Technical data

Dimensions

Width	6.2 mm
Height	80 mm
Depth	56 mm

General

Supply voltage range	20 V DC ... 30 V DC (U_V)
Continuous current	200 mA
Residual voltage drop	< 1 V
Leakage current	< 1 mA
Max. transmission frequency	15 kHz

NPN input/PNP output

Switch-on threshold	< 5 V (at $U_V = 24$ V; < ($U_V - 19$ V))
Switch-off threshold	> 15 V (at $U_V = 24$ V; > ($U_V - 9$ V))
Min. limit values	-2 V
Max. limit values	26 V (at $U_V = 24$ V; $U_V + 2$ V)

PNP input/NPN output

Switch-on threshold	> 19 V
Switch-off threshold	< 9 V
Min. limit values	-2 V

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Technical data

PNP input/NPN output

Max. limit values	26 V (at $U_V = 24\text{ V}$; $U_V + 2\text{ V}$)
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Connection data

Conductor cross section solid min.	0.2 mm ²
Conductor cross section solid max.	4 mm ²
Conductor cross section stranded min.	0.2 mm ²
Conductor cross section stranded max.	2.5 mm ²
Conductor cross section AWG/kcmil min.	24
Conductor cross section AWG/kcmil max	12
Connection method	Screw connection
Stripping length	8 mm
Screw thread	M3

Ambient conditions

Ambient temperature (operation)	-20 °C ... 50 °C
Ambient temperature (storage/transport)	-20 °C ... 70 °C

classifications

eCl@ss

eCl@ss 4.0	27371102
eCl@ss 4.1	27371102
eCl@ss 5.0	27371001
eCl@ss 5.1	27371001
eCl@ss 6.0	27371001
eCl@ss 7.0	27371001
eCl@ss 8.0	27371001

ETIM

ETIM 2.0	EC001437
ETIM 3.0	EC001437
ETIM 4.0	EC001437
ETIM 5.0	EC001437

UNSPSC

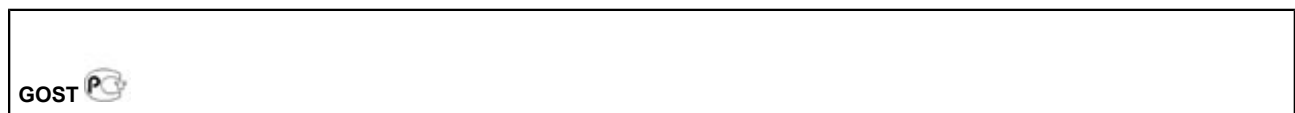
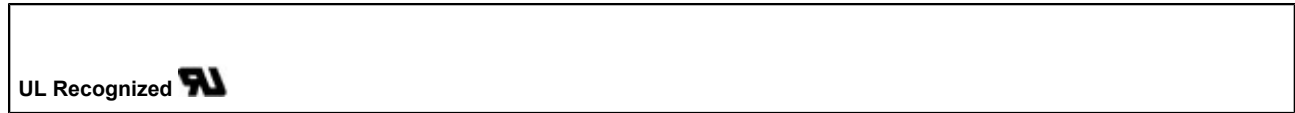
UNSPSC 6.01	30211916
UNSPSC 7.0901	39121515
UNSPSC 11	39121515
UNSPSC 12.01	39121515
UNSPSC 13.2	39121515

approvals

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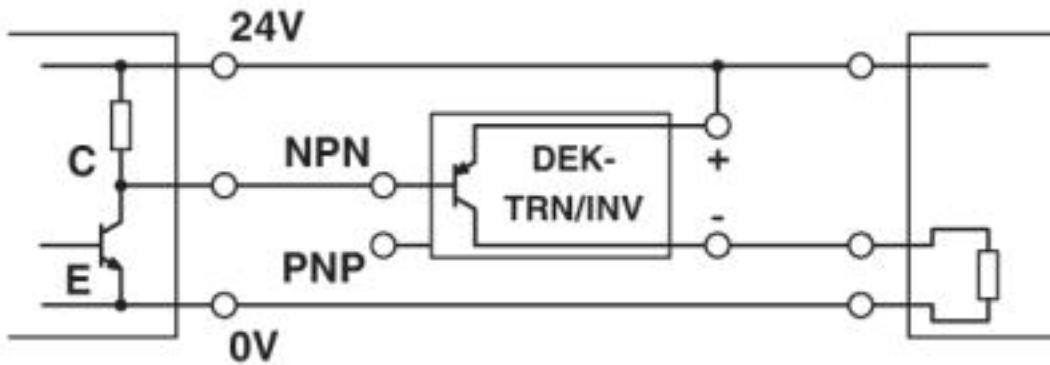
approvals

Approval details



Drawings

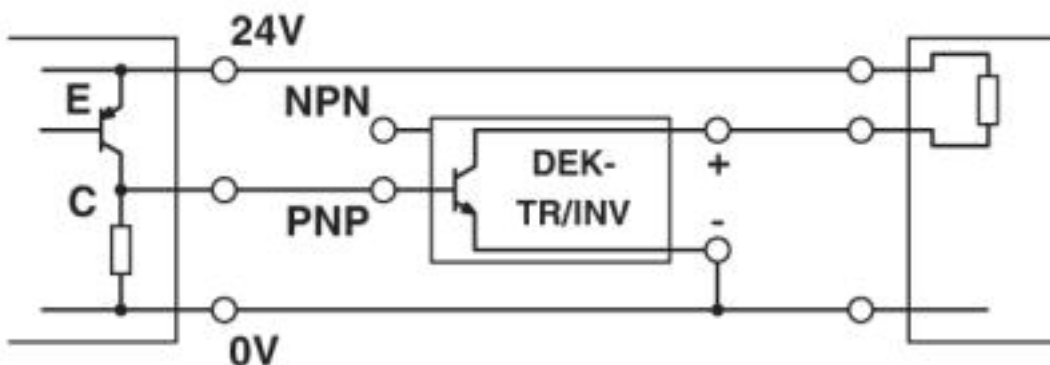
Application drawing



a = NPN output
b = load

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Application drawing



Circuit diagram

