



ATTENTION
OBSERVE PRECAUTIONS
FOR HANDLING
ELECTROSTATIC
DISCHARGE
SENSITIVE
DEVICES

Part Number: KTDS-3535SE9Z4S

Reddish-Orange

### Absolute Maximum Ratings at TA = 25°C

Parameter	Symbol	Value	Unit
DC Forward Current [1]	lF	500	mA
Peak Forward Current [2]	lғм	800	mA
Power dissipation	Po	1.65	W
Reverse Voltage	VR	5	V
Operating Temperature	Тор	-40 To +100	°C
Storage Temperature	Tstg	-40 To +110	°C
Junction temperature[1]	TJ	110	°C
Thermal resistance [1] (Junction/ambient)	Rth j-a	50	°C/W
Thermal resistance [1] (Junction/solder point)	Rth j-s	12	°C/W

#### Notes:

# Electrical / Optical Characteristics at TA = 25°C

Parameter	Symbol	Value	Unit
Wavelength at peak emission  IF = 350mA [Typ.]	λpeak	635	nm
Dominant Wavelength IF = 350mA [Typ.]	λ dom [1]	623	nm
Spectral bandwidth at 50% Prel MAX IF = 350mA [Typ.]	Δλ	22	nm
Allowable Reverse Current [Max.]	lR	85	mA
Forward Voltage IF = 350mA [Min.]		2.0	
Forward Voltage IF = 350mA [Typ.]	VF [2]	2.5	V
Forward Voltage IF = 350mA [Max.]		3.0	
Temperature coefficient of λpeak  IF = 350mA, -10°C≤ T≤100°C  [Typ.]	TCλpeak	0.10	nm/°C
Temperature coefficient of $\lambda$ dom IF = 350mA, -10°C $\leq$ T $\leq$ 100°C [Typ.]	TCλdom	0.08	nm/°C
Temperature coefficient of VF IF = 350mA, -10°C≤ T≤100°C [Typ.]	TCv	-3.0	mV/°C

#### Notes:

- 1.Wavelength: +/-1nm.
- 2. Forward Voltage: +/-0.1V.
- ${\it 3. Wavelength\ value\ is\ traceable\ to\ the\ CIE127-2007\ compliant\ national\ standards.}$





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<sup>1.</sup> Results from mounting on metal core PCB, mounted on pc board-metal core PCB is recommend for lowest thermal resistance.

<sup>2. 1/10</sup> Duty Cycle, 0.1ms Pulse Width.



#### Selection Guide

Part No.	Dice	Фv (lm) [2] @ 350mA				Viewing Angle [1]
		Code.	Min.	Max.	Тур.	201/2
KTDS-3535SE9Z4S	Reddish-Orange (AlGaInP)	В9	42	50	- 53	110°
N1D3-33335E9243		B10	50	60		110-

#### Notes:

- 1.  $\theta$ 1/2 is the angle from optical centerline where the luminous intensity is 1/2 of the optical peak value.
- 2. Luminous intensity / luminous flux: +/-15%
- 3. Luminous Flux value is traceable to the CIE127-2007 compliant national standards.

### **Package Dimension And Materials**

For package dimension please refer to page 9

Material as follows: Package : Ceramics

Encapsulating resin : Silicone resin
Electrodes : Ag plating

#### **Features**

- 1.Dimensions: 3.45mm X 3.45mm X 2.0mm.
- 2.Small package with high efficiency.
- 3. Surface mount technology.
- 4. Soldering methods: IR reflow soldering.
- 6.Moisture sensitivity level: level 1.
- 7. Electrically neutral thermal path.
- 8.RoHS compliant.

# Packaging:

- 1. The LEDs are packed in cardboard boxes after taping.
- 2. The label on the minimum packing unit shows: Part Number, Lot Number, Ranking, Quantity.
- 3. In order to protect the LEDs from mechanical shock, we pack them in cardboard boxes for transportation.
- 4. The LEDs may be damaged if the boxes are dropped or receive a strong impact against them, so precautions must be taken to prevent any damage.
- 5. The boxes are not water resistant and therefore must be kept away from water and moisture.
- $6. When the \ LEDs \ are \ transported, \ we \ recommend \ that \ you \ use \ the \ same \ packing \ methods \ as \ Kingbright's.$

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# **Reliability Test Items And Conditions**

The reliability of products shall be satisfied with items listed below

Lot Tolerance Percent Defective (LTPD): 10%

No.	Test Item	Standards	Test Condition	Test Times / Cycles	Number of Damaged
1	Continuous operating test	-	Ta =25°C +10/-5°C ,RH=55+/-20%RH IF = maximum rated current*	1,000 h	0 / 22
2	High Temp. operating test	1	Ta = 100°C(+/-10°C) IF = maximum rated current*	1,000 h	0 / 22
3	Low Temp. operating test	-	Ta = -40°C+3/-5°C IF = maximum rated current*	1,000 h	0 / 22
4	High temp. storage test	JEITA ED- 4701/200 201	Ta = 100°C(+/-10°C) Ta = maximum rated storage temperature	1,000 h	0 / 22
5	Low temp. storage test	JEITA ED- 4701/200 202	Ta = -40°C+3/-5°C	1,000 h	0 / 22
6	High temp. & humidity storage test	JEITA ED- 4701/100 103	Ta = 60°C+5/-3°C, RH = 90+5/-10%RH	1,000 h	0 / 22
7	High temp. & humidity operating test	-	Ta = 60°C+5/-3°C, RH = 90%+5/-10%RH IF = maximum rated current*	1,000h	0 / 22
8	Resistance to Soldering Heat (Reflow Soldering)	JEITA ED- 4701/300 301	Tsld=260°C,10sec	2 times	0 / 22
9	Solderability (Reflow Soldering)	JEITA ED- 4701/303 303A	Tsld=245°C+/-5°C,5+/-1sec	1 time over 95%	0 / 22
10	Temperature Cycle operating test	-	-40°C(30min) ~25°C(5min)~100°C(30min) ~25°C(5min) IF = derated current at 100°C	10cycles	0 / 22
11	Temperature Cycle	JEITA ED- 4701/100 105	-40°C(30min) ~25°C(5min)~100°C(30min) ~25°C(5min)	100cycles	0 / 22
12	Thermal shock test	MIL-STD-202G	Ta = -40°C(15min) ~100°C(15min)	500 cycles	0 / 22
13	Electric Static Discharge (ESD)	JEITA ED- 4701/300 304	C = 100pF , R= 1.5KΩ V = 2kV	3 times Negative/ Positive	0 / 22
14	Vibration test	JEITA ED- 4701/400 403	100~2000~100HZ Sweep 4min. 200m/s² 3directions,4cycles	48min.	0 / 22

Note: Refer to forward current vs. derating curve diagram.

# **Criteria For Judging Damage**

Item	Symbol	Test Conditions	Criteria for Judgement		
Item		rest Conditions	Min.	Max.	
Forward Voltage	VF	IF = 350mA	-	Initial Level x 1.1	
Luminous Flux	Ф۷	IF = 350mA	Initial Level x 0.7	-	

Note: The test is performed after the board is cooled down to the room temperature.

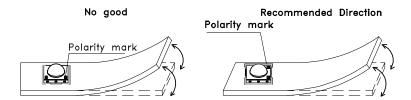
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### Designing the Position of LED on a Board.

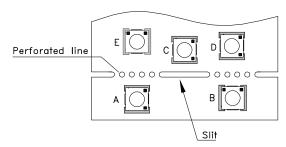
1.No twist/warp/bent/or other stress shall be applied to the board after mounting LED with solder to avoid a crack of LED package.

Refer to the following recommended position and direction of LED.



Appropriate LED mounting is to place perpendicularly against the stress affected side.

2.Depending on the position and direction of LED,the mechanical stress on the LED package can be changed. Refer to the following figure.



Stress: A>B=C>D>E

- 3.Do not split board by hand. Split with exclusive special tool.
- 4.If an aluminum circuit board is used, a large stress by thermal shock might cause a solder crack.

For this reason, it is recommended an appropriate verification should be taken before use.

# **JEDEC Moisture Sensitivity:**

Level	Floor Life		Soak Requirements			
			Standard		Accelerated Equivalent	
	Time	Conditions	Time (hours)	Conditions	Time (hours)	Conditions
1	Unlimited	≤ 30 °C / 85% RH	168 + 5 / - 0	85 °C / 85% RH		

Kingbright recommends keeping the LEDs in the sealed moisture-barrier packaging until immediately prior to use. Any unused LEDs should be returned to the moisture-barrier bag and closed immediately after use.

#### **ESD Protection During Production**

Electric static discharge can result when static-sensitive products come in contact with the operator or other conductors.

The following procedures may decrease the possibility of ESD damage:

- 1. Minimize friction between the product and surroundings to avoid static buildup.
- 2.All production machinery and test instruments must be electrically grounded.
- 3. Operators must wear anti-static bracelets.
- 4. Wear anti-static suit when entering work areas with conductive machinery.
- 5.Set up ESD protection areas using grounded metal plating for component handling.
- 6.All workstations that handle IC and ESD-sensitive components must maintain an electrostatic potential of 150V or less.
- 7. Maintain a humidity level of 50% or higher in production areas.
- 8. Use anti-static packaging for transport and storage.
- 9.All anti-static equipment and procedures should be periodically inspected and evaluated for proper functionality.

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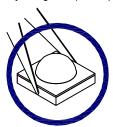


### **Handling Precautions**

Compare to epoxy encapsulant that is hard and brittle, silicone is softer and flexible. Although its characteristic significantly reduces thermal stress, it is more susceptible to damage by external mechanical force.

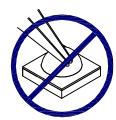
As a result, special handling precautions need to be observed during assembly using silicone encapsulated LED products. Failure to comply might lead to damage and premature failure of the LED.

1. Handle the component along the side surfaces by using forceps or appropriate tools.



2. Do not directly touch or handle the silicone lens surface. It may damage the internal circuitry.



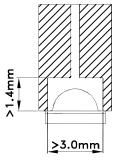




3. Do not stack together assembled PCBs containing exposed LEDs. Impact may scratch the silicone lens or damage the internal circuitry.



- 4.1. There should be enough space inside the nozzle to avoid contact with the dome lens during pick up.
- 4.2. The inner diameter of the SMD pickup nozzle should not exceed the size of the LED to prevent air leaks.
- 4.3. A pliable material is suggested for the nozzle tip to avoid scratching or damaging the LED surface during pickup.
- 4.4. The dimensions of the component must be accurately programmed in the pick-and-place machine to insure precise pickup and avoid damage during production.



5. As silicone encapsulation is permeable to gases, some corrosive substances such as  $H_2S$  might corrode silver plating of leadframe. Special care should be taken if an LED with silicone encapsulation is to be used near such substances.

Detailed application notes are listed on our website. http://www.kingbright.com/application\_notes

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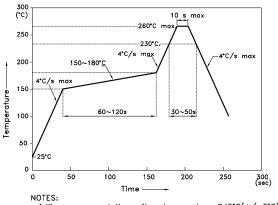
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Reflow soldering is recommended and the soldering profile is shown below.

Other soldering methods are not recommended as they might cause damage to the product.

Reflow Soldering Profile For Lead-free SMT Process.



- 1.We recommend the reflow temperature 245°C(+/-5°C).The maximum soldering temperature should be limited to 260°C.
- 2.Don't cause stress to the epoxy resin while it is exposed

### 3. Number of reflow process shall be 2 times or less.

### **Heat Generation:**

1. Thermal design of the end product is of paramount importance. Please consider the heat generation of the LED when making the system design. The coefficient of temperature increase per input electric power is affected by the thermal resistance of the circuit board and density of LED placement on the board ,as well as other components. It is necessary to avoid intense heat generation and operate within the maximum ratings given in this specification.

2.Please determine the operating current with consideration of the ambient temperature local to the LED and refer to the plot of Permissible Forward current vs. Ambient temperature on CHARACTERISTICS in this specification. Please also take measures to remove heat from the area near the LED to improve the operational characteristics on the LED.

3.The equation ① indicates correlation between T<sub>j</sub> and T<sub>a</sub> ,and the equation ② indicates correlation between T<sub>j</sub> and T<sub>s</sub>

 $T_j = Ta + Rthj-a *W$  .....

Tj = Ts + Rthj-s \*W ...... 2

Tj = dice junction temperature: °C

Ta = ambient temperature: °C

Ts = solder point temperature: °C

Rthj-a = heat resistance from dice junction temperature to ambient temperature: °C / W

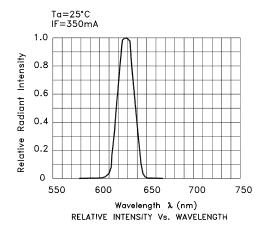
Rthj-s = heat resistance from dice junction temperature to Ts measuring point :  $^{\circ}$ C / W

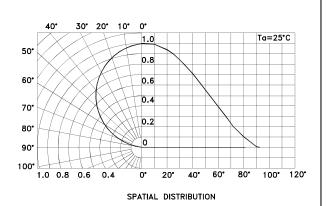
W = inputting power (IFx VF) : W

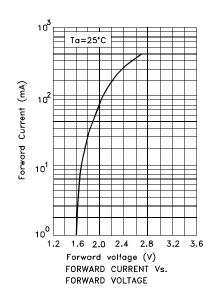
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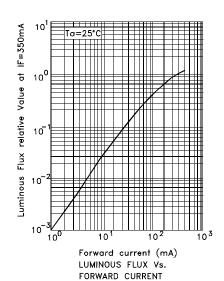


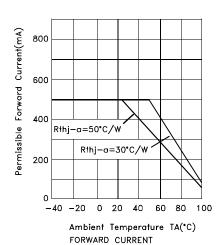
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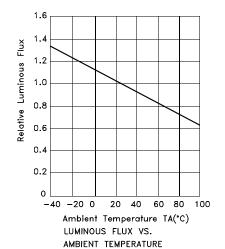






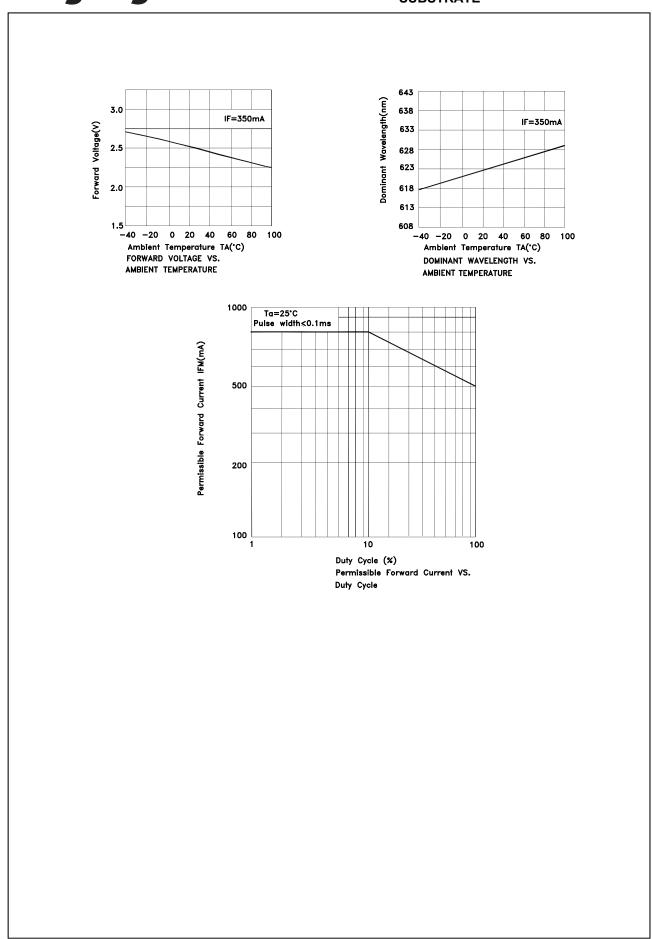


DERATING CURVE



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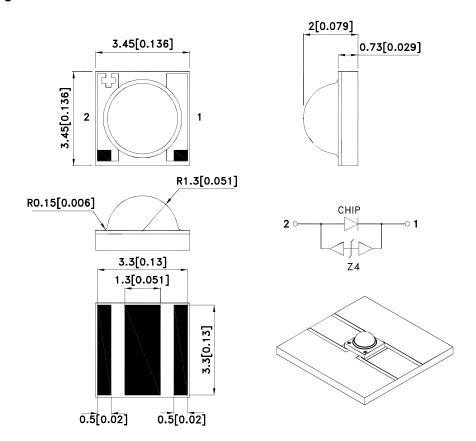




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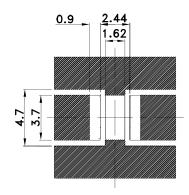
# **Package Dimensions**

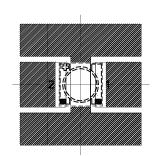


- Notes:
  1. All dimensions are in millimeters (inches).
  2. Tolerance is ±0.2(0.008") unless otherwise noted.
- 3. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.

  4. The device has a single mounting surface. The device must be mounted according to the specifications.

# **Recommended Soldering Pattern** (Units: mm; Tolerance: ± 0.1)





Solder resist

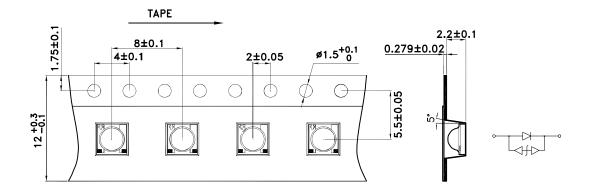
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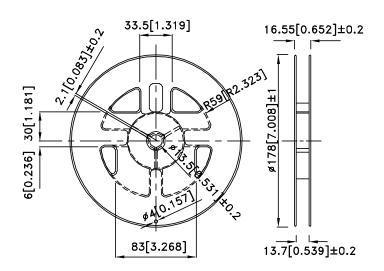
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# Tape Dimensions (Units: mm)

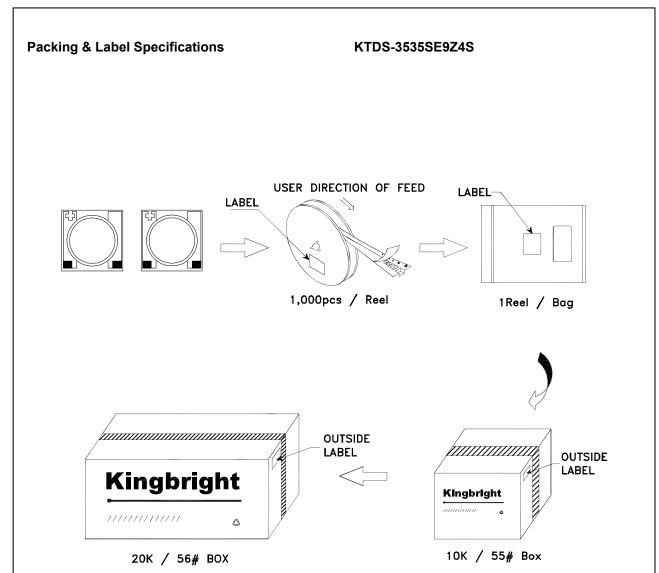


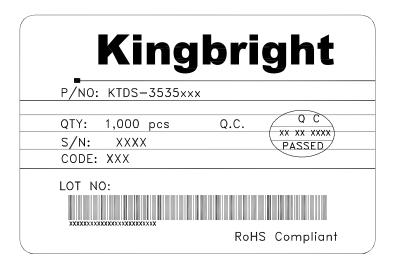
### **Reel Dimension**



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