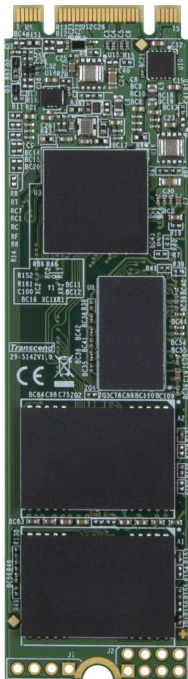


MTS950T – SATA III 6Gb/s SSD

Due to Ultra-slim (fit the standard dimensions of 2280-D2-B-M), huge capacity, SATA 6Gb/s speed, and low power consumption, Solid State Drive is the perfect replacement storage device for PCs, Ultrabooks, Laptops, gaming systems, and hand-held devices.



Features

- Advanced Global Wear-Leveling and Block management for reliability
- Supports Advanced Garbage Collection
- Built-in LDPC ECC (Error Correction Code) functionality
- Supports Enhanced S.M.A.R.T. Function
- Feature a DDR3 DRAM cache
- RoHS compliant
- Provides great shock resistance
- Slim, elegant, light design.
- Non-volatile Flash Memory for outstanding data retention
- Supports Trim and NCQ command
- Supports DEVSLP mode (optional)

Specifications

Physical Specification			
Form Factor		M.2 TYPE 2280-D2-B-M	
Storage Capacities		64 GB, 128 GB, 256 GB, 512GB, 1TB	
Dimensions	Length	80.00 ± 0.15 mm	3.15 ± 0.006 inch
	Width	22.00 ± 0.15 mm	0.866 ± 0.006 inch
	Height	Max 3.58 mm	Max 0.1409 inch
Interface		SATA 6 Gb/s	
Input Voltage		3.3V ±5%	
Weight (MAX)		Max. 9 g	
Connector		M.2 TYPE 2280-D2-B-M	

Environmental Specifications	
Operating Temperature	Commercial (0 °C to +70 °C)
Humidity	5% to 95%, non-condensing
Vibration	5 - 800 Hz, 20 G (peak-to-peak)
Shock	1500 G, 0.5 ms, 3axis
Warranty	3 years limited

Performance								
Model P/N	ATTO		CrystalDiskMark				IOMeter	
	Max. Read (MB/s)	Max. Write (MB/s)	Sequential Read(QD32) (MB/s)	Sequential Write(QD32) (MB/s)	Random Read (4KB QD32) (MB/s)	Random Write (4KB QD32) (MB/s)	IOPS Random Read (4KB QD32)	IOPS Random Write (4KB QD32)
TS64GMTS950T	390	200	390	200	120	90	35K	55K
TS128GMTS950T	550	400	550	400	200	180	55K	70K
TS256GMTS950T	550	520	550	520	300	350	85K	85K
TS512GMTS950T	560	510	560	510	300	350	85K	85K
TS1TMTS950T	560	520	560	520	350	350	85K	85K

Note: Maximum transfer speed recorded

* 25 °C, test on ASUS P8Z68-M PRO, 4 GB, Windows[®] 7 Professional with AHCI mode, benchmark utility ATTO (version 3.0.5), unit MB/s

** 25 °C, test on ASUS P8Z68-M PRO, 4 GB, Windows[®] 7 Professional with AHCI mode, benchmark utility CrystalDiskMark (version 5.1.2), copied file 1000MB, unit MB/s

*** 25 °C, test on ASUS P8Z68-M PRO, 4 GB, Windows[®] 7 Professional with AHCI mode, benchmark utility IOMeter2006 with 4K file size and queue depth of 32, unit IOPs

**** The recorded performance is obtained while the SSD is not operating as an OS disk

Power Requirements		
Input Voltage		3.3V ± 5% @ 25 °C
Mode P/N / Power Consumption		Typical (W)
DEVSLP		5 mW
TS64GMTS950T	Max Write*	1.2
	Max Read*	1
	Idle*	0.3
TS128GMTS950T	Max Write*	1.4
	Max Read*	1
	Idle*	0.3
TS256GMTS950T	Max Write*	1.8
	Max Read*	1.6
	Idle*	0.3
TS512GMTS950T	Max Write*	2
	Max Read*	1.9
	Idle*	0.3
TS1TMTS950T	Max Write*	1.9
	Max Read*	1.2
	Idle*	0.3

Power on to Ready	
Power on to Ready	0.22s

Note: 1. Using Drive Master and power on to ready with proper shutdown condition

2. Enable standby immediately

Reliability			
Data Retention	1 years		
MTBF	1 million hours		
Endurance (Terabytes Written)	Capacity	*TBW	**TBW (base on JEDEC Standard)
	64 GB	40	20
	128 GB	80	40
	256 GB	160	80
	512 GB	320	160
	1TB	640	320
DWPD (Drive Writes Per Day for 3years)	0.58 DWPD		

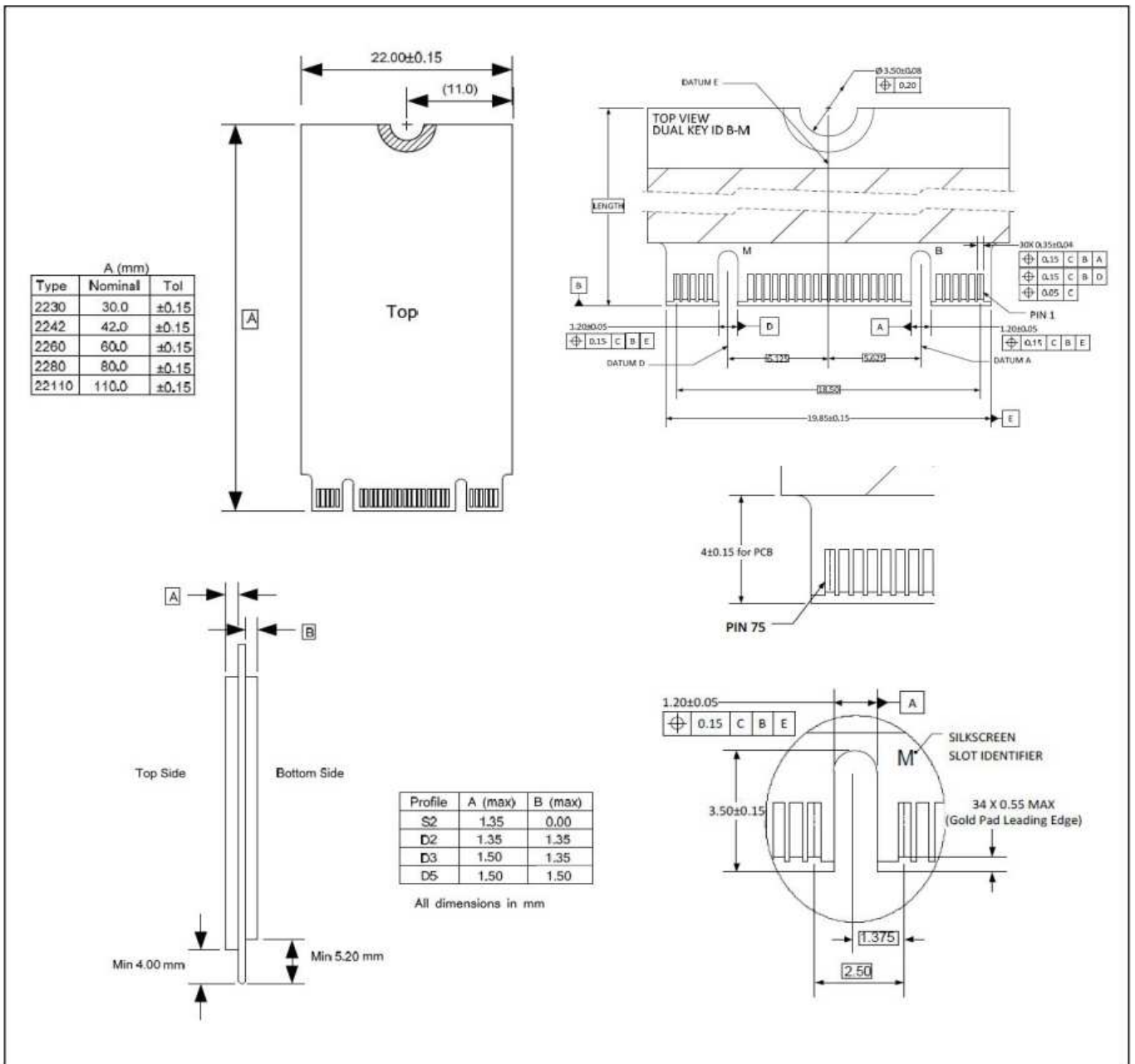
*Tested under burn-in tool, TBW value may vary due to host environment

**Tested under JESD219A endurance workloads specification

Regulations	
Compliance	CE, FCC and BSMI

Package Dimensions

The figure below illustrates the Transcend M.2 Type 2280-D2-B-M Solid State Drive. All dimensions are in mm.



Pin Assignments

Pin No.	Pin Name	Pin No.	Pin Name	Pin No.	Pin Name	Pin No.	Pin Name
01	CONFIG_3*	02	3.3V	39	GND	40	NC
03	GND	04	3.3V	41	TX+	42	NC
05	NC	06	NC	43	TX-	44	NC
07	NC	08	NC	45	GND	46	NC
09	NC	10	DAS/DSS**	47	RX-	48	NC
11	NC	12	NOTCH	49	RX+	50	NC
13	NOTCH	14	NOTCH	51	GND	52	NC
15	NOTCH	16	NOTCH	53	NC	54	NC
17	NOTCH	18	NOTCH	55	NC	56	MFG1****
19	NOTCH	20	NC	57	GND	58	MFG2****
21	CONFIG_0*	22	NC	59	NOTCH	60	NOTCH
23	NC	24	NC	61	NOTCH	62	NOTCH
25	NC	26	NC	63	NOTCH	64	NOTCH
27	GND	28	NC	65	NOTCH	66	NOTCH
29	NC	30	NC	67	NC	68	NC
31	NC	32	NC	69	CONFIG_1*	70	3.3V
33	GND	34	NC	71	GND	72	3.3V
35	NC	36	NC	73	GND	74	3.3V
37	NC	38	DEVSLP***	75	CONFIG_2*		

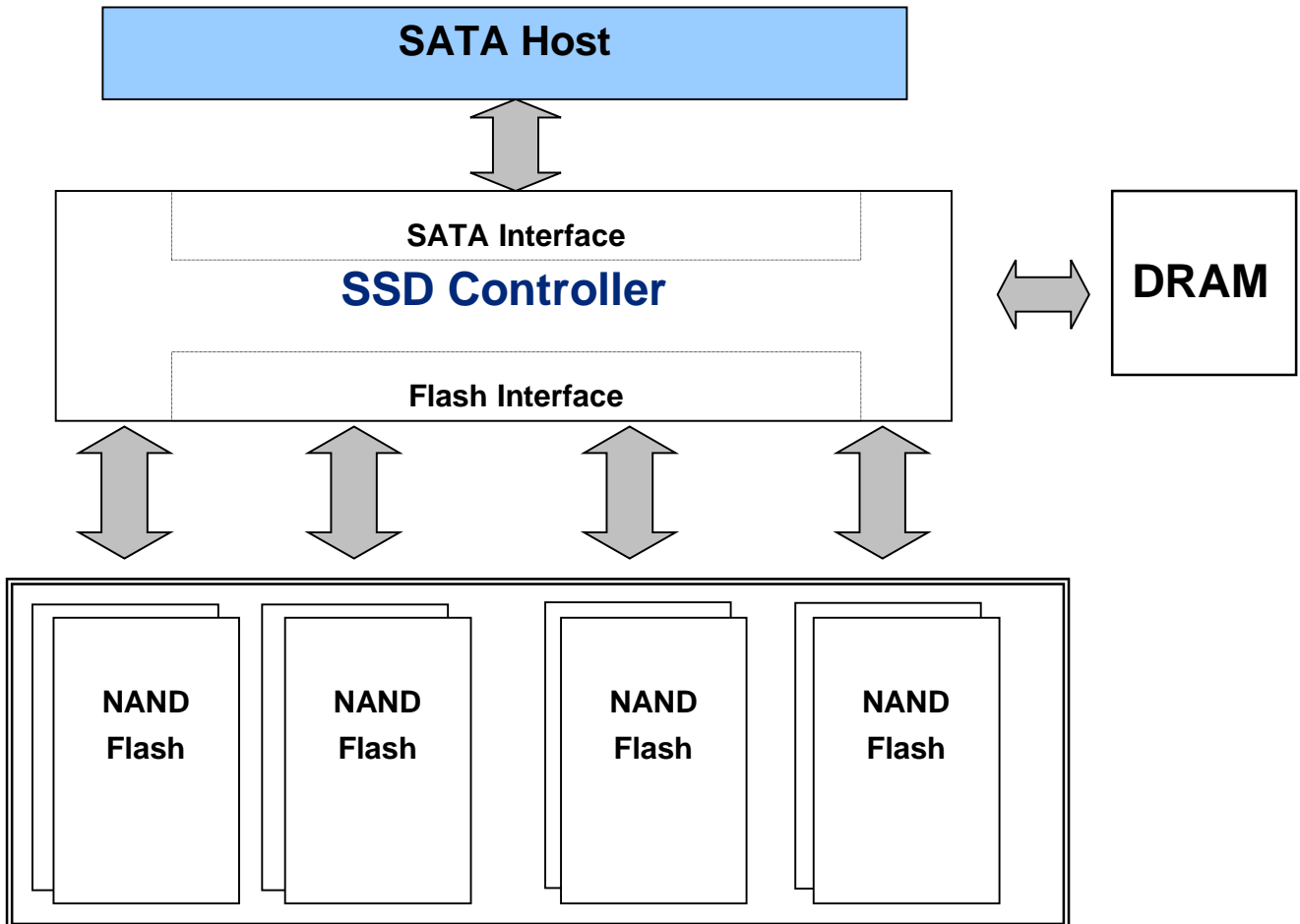
* For SATA application, these pins connect to GND internally

** Device Activity Signal / Disable Staggered Spin-up

*** Device Sleep, Input. If driven high the host is informing the SSD to enter a low power state

**** Manufacturing pins. Do not connect

Block Diagram



Features

- **Global Wear Leveling – Advanced algorithm to enhance the Wear-Leveling Efficiency**

Global wear leveling ensures that every block has an even erase count. This helps to extend the life expectancy

- **Advanced Garbage Collection**

Transcend's SSDs have a perfect garbage collection mechanism to help improve performance. Advanced Garbage collection can efficiently improve memory management to ensure the SSD's stable performance. With Transcend advanced flash management, the drive can still keep high performance even after a long operating time.

- **LDPC ECC**

The LDPC ECC engine executes parity generation and error detection/correction features, and enhances decoding throughput and data reliability. With LDPC of correction 1e-2 RBER, the hard and soft decoding mechanism provides powerful error correction. Hence the controller can enhance the endurance and retention of TLC NAND and extends the SSD lifespan.

- **RAID Protection**

In case of uncorrectable errors occurring within a superblock (a pre-defined area which consist of a particular set of blocks across physical NAND units), the RAID engine recovers the uncorrectable error chunk by using a certain storage space of parity bits. Incorporated with LDPC, the RAID ensures a comprehensive level of data integrity while providing a broad range of RAID overhead protection

- **Enhanced S.M.A.R.T. function**

Transcend SSD supports S.M.A.R.T. command (Self-Monitoring, Analysis, and Reporting Technology) that allows the user to read the health information of the SSD. Transcend also define some innovated S.M.A.R.T. features which allows the user to evaluate the status of the SSD in a much more efficient way.

- **DEVSLP (optional)**

DEVSLP is a new host-controlled SATA interface power state which together enables a SATA host and device to enter an ultra-low interface power state, including the possibility of completely powering down host and device PHYs.

ATA Command Register

This table with the following paragraphs summarizes the ATA command set.

Command Table

Name	Code	Subcode / Page
NOP	00h	
Data Set Management	06h	
Trim		01h
Recalibrate	1Xh	
Read Sectors	20h	
Read Sectors (w/o retry)	21h	
Read Sectors Ext	24h	
Read DMA Ext	25h	
Read Native Max Address Ext	27h	
Read Multiple Ext	29h	
Read Log Ext	2Fh	
Log Directory		00h
Extended Comprehensive SMART Error Log		03h
Device Statistics Logs		04h
<i>List of supported log pages</i>		00h
<i>General Statistics</i>		01h
<i>General Errors Statistics</i>		04h
<i>Transport Statistics</i>		06h
SSD Statistics		07h
Extended SMART Self-test Log		07h
NCQ Error Log		10h
SATA Phy Event Counters Log		11h
Identify Device Data Log		30h
<i>List of Supported Pages</i>		00h
<i>Copy of IDENTIFY DEVICE Data</i>		01h

<i>Capacity</i>		02h
<i>Supported Capabilities</i>		03h
<i>Current Settings</i>		04h
<i>ATA Strings</i>		05h
<i>Security</i>		06h
<i>Serial ATA</i>		08h
Write Sectors	30h	
Write Sectors Ext	34h	
Write DMA Ext	35h	
Set Max Address Ext	37h	
Write Multiple Ext	39h	
Write DMA FUA Ext	3Dh	
Write Log Ext	3Fh	
Selective Self-Test log(SMART)		09h
Host Specific(SMART)		80h~9Fh
SCT Command/Status(SCT)		E0h
SCT Data Transfer(SCT)		E1h
Read Verify Sectors	40h	
Read Verify Sectors (w/o retry)	41h	
Read Verify Sectors Ext	42h	
Write Uncorrectable Ext	45h	
Pseudo-UECC with logging		55h
Read FPDMA Queued	60h	
Write FPDMA Queued	61h	
Seek	7xh	
Execute Device Diagnostic	90h	
Initialize Drive Parameters	91h	
Download Microcode	92h	
Download with offsets and save microcode for immediate and future use.		03h
Download (without offsets) and save microcode		07h

Download with offsets and save microcode for future use / Activate downloaded microcode		0Eh/0Fh
SMART	B0h	
Read Data		D0h
Read Thresholds		D1h
Enable/Disable Attr Autosave		D2h
Exec Off-line Immediate		D4h
<i>Execute Off-Line routine</i>		00h
<i>Execute Short Self-test routine (Off-Line)</i>		01h
<i>Execute Extended Self-test routine (Off-Line)</i>		02h
<i>Abort Off-Line Self-test routine</i>		7Fh
<i>Execute Short Self-test routine (Captive)</i>		81h
<i>Execute Extended Self-test routine (Captive)</i>		82h
Read Log Sector		D5h
Write Log Sector		D6h
Enable Operations		D8h
Disable Operations		D9h
Return Status		Dah
Sanitize Device	B4h	
Sanitize Status Ext		00h
Block Erase Ext		12h
Sanitize Freeze Lock Ext		20h
Read Multiple	C4h	
Write Multiple	C5h	
Set Multiple Mode	C6h	
Read DMA	C8h	
Read DMA (w/o retry)	C9h	
Write DMA	CAh	
Write DMA (w/o retry)	CBh	
Write Multiple FUA Ext	CEh	
Standby Immediate	E0h	

Idle Immediate	E1h	
Standby	E2h	
Idle	E3h	
Read Buffer	E4h	
Check Power Mode	E5h	
Sleep	E6h	
Flush Cache	E7h	
Write Buffer	E8h	
Flush Cache Ext	EAh	
Identify Device	ECh	
Set Features	EFh	
Security Set Password	F1h	
Security Unlock	F2h	
Security Erase Prepare	F3h	
Security Erase Unit	F4h	
Security Freeze Lock	F5h	
Security Disable Password	F6h	
Read Native Max Address	F8h	
Set Max Address	F9h	
Set Max Set Password		01h
Set Max Lock		02h
Set Max Unlock		03h
Set Max Freeze Lock		04h
Set Max Set Password DMA		05h
Set Max Unlock DMA		06h

SMART Data Structure

BYTE	F / V	Description
0-1	X	Revision code
2-361	X	Vendor specific
362	V	Off-line data collection status
363	X	Self-test execution status byte
364-365	V	Total time in seconds to complete off-line data collection activity
366	X	Vendor specific
367	F	Off-line data collection capability
368-369	F	SMART capability
370	F	Error logging capability 7-1 Reserved 0 1=Device error logging supported
371	X	Vendor specific
372	F	Short self-test routine recommended polling time (in minutes)
373	F	Extended self-test routine recommended polling time (in minutes)
374	F	Conveyance self-test routine recommended polling time (in minutes)
375-385	R	Reserved
386-395	F	Firmware Version/Date Code
396-399	F	Reserved
400-409	V	SMI2258TLC
410-510	X	Vendor specific
511	V	Data structure checksum

F = content (byte) is fixed and does not change.

V= content (byte) is variable and may change depending on the state of the device or the commands executed by the device.

X= content (byte) is vendor specific and may be fixed or variable.

R= content (byte) is reserved and shall be zero.

SMART Attributes

The following table shows the vendor specific data in byte 2 to 361 of the 512-byte SMART data

Attribute ID (hex)	Raw Attribute Value							Attribute Name
01	MSB	00	00	00	00	00	00	Read Error Rate
05	LSB	MSB	00	00	00	00	00	Reallocated sectors count
09	LSB	-	-	MSB	00	00	00	Power-on hours
0C	LSB	-	-	MSB	00	00	00	Power Cycle Count
94	LSB	-	-	MSB	00	00	00	SLC Total Erase Count
95	LSB	-	-	MSB	00	00	00	SLC Max Erase Count
96	LSB	-	-	MSB	00	00	00	SLC Min Erase Count
97	LSB	-	-	MSB	00	00	00	SLC Average Erase Count
9F	LSB	-	-	MSB	00	00	00	DRAM one bit error count
A0	LSB	-	-	MSB	00	00	00	Uncorrectable sectors count when read/write
A1	LSB	MSB	00	00	00	00	00	Number of valid spare blocks
A3	LSB	MSB	00	00	00	00	00	Number of initial invalid blocks
A4	LSB	-	-	MSB	00	00	00	TLC Total erase count
A5	LSB	-	-	MSB	00	00	00	TLC Maximum erase count
A6	LSB	-	-	MSB	00	00	00	TLC Minimum erase count
A7	LSB	-	-	MSB	00	00	00	TLC Average erase count
A8	LSB	-	-	MSB	00	00	00	Max Erase count of spec
A9	LSB	-	-	MSB	00	00	00	Remain Life (percentage)
B1	LSB	-	-	MSB	00	00	00	Total wear level count
B5	LSB	-	-	MSB	00	00	00	Total program fail count
B6	LSB	MSB	00	00	00	00	00	Total erase fail count
C0	LSB	MSB	00	00	00	00	00	Power-off retract Count
C2	MSB	00	00	00	00	00	00	Enclosure temperature
C3	LSB	-	-	MSB	00	00	00	Hardware ECC recovered
C4	LSB	-	-	MSB	00	00	00	Reallocation event count
C7	LSB	MSB	00	00	00	00	00	Ultra DMA CRC Error Count
E8	LSB	MSB	00	00	00	00	00	Available reserved space
F1	LSB	-	-	-	-	-	MSB	Total LBA written (each write unit = 32MB)
F2	LSB	-	-	-	-	-	MSB	Total LBA read (each read unit = 32MB)
F5	LSB	-	-	-	-	-	MSB	Flash write sector count

Ordering Information

Capacity	Model P/N
64GB	TS64GMTS950T
128GB	TS128GMTS950T
256GB	TS256GMTS950T
512GB	TS512GMTS950T
1TB	TS1TMTS950T

The technical information above is based on industry standard data and has been tested to be reliable. However, Transcend makes no warranty, either expressed or implied, as to its accuracy and assumes no liability in connection with the use of this product. Transcend reserves the right to make changes to the specifications at any time without prior notice.



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Revision History		
Version	Date	Modification Content
V1.0	2017/12/25	Initial Release
V1.1	2018/01/05	Update TBW and remove SLC cache feature
V1.2	2018/01/12	IO-meter performance
V1.3	2018/02/01	Add 512GB Information
V1.4	2018/07/26	Add DRAM feature in product features
V1.5	2018/07/31	Update product picture and DWPD
V1.6	2019/04/16	Add 1TB Information, Power on to Ready Info, and update unit for Power Consumption
V1.7	2019/06/04	Update Weight Information, and Power Consumption for 1TB