

10 9 8 7 6 5 4 3 2 1

NOTES:

1. MATERIALS

HOUSING - HIGH TEMP. NYLON GLASS FILLED, UL 94 V-0, BLACK.
EJECTOR - HIGH TEMP. NYLON GLASS FILLED, UL 94 V-0, BLACK.
TERMINAL - COPPER ALLOY

2. PLATING - SEE TABLE IN SHEET 5.

3. CARD SLOT ACCEPTS 1.27±0.10 MM MODULE THICKNESS.
(MEASURED OVER P.C. PADS)

4. RECOMMENDED MODULE LAYOUT SHALL BE AS PER JEDEC MO-237.

5. REFER TO PRODUCT SPEC. PS-87978-013 FOR PERFORMANCE SPECIFICATIONS.

6. PRODUCT SHALL BE PACKED IN TRAY.

7. PRODUCT SHALL HAVE DATE CODE STAMPED ON SIDE OF HOUSING.

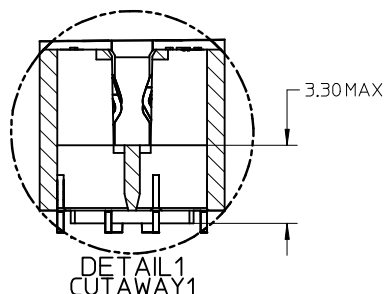
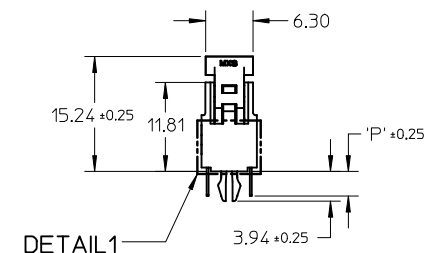
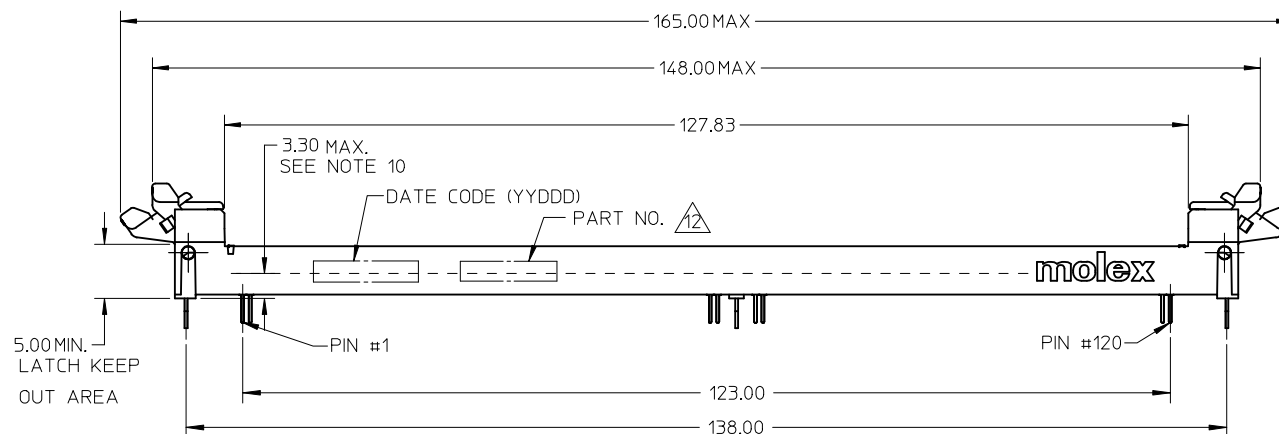
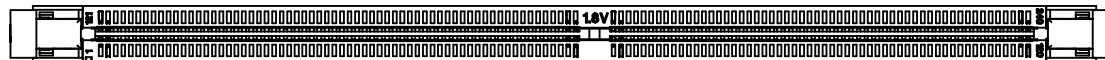
8. KEEP OUT AREA IS THE AREA WHERE THE CONNECTOR IS MOUNTED ONTO THE PCB WHICH DOES NOT INCLUDE THE LATCH.

9. REFER TO CRITICAL JEDEC INFORMATION FOR SOCKET OUTLINE PER SO-001.

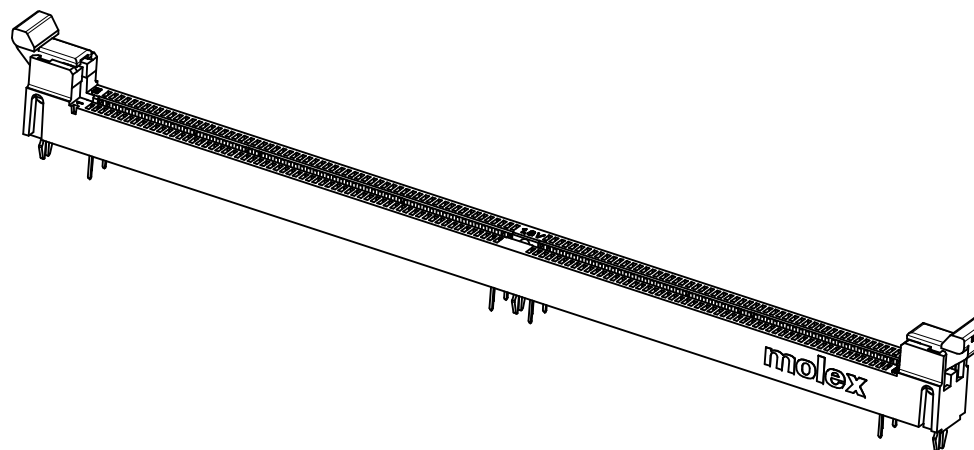
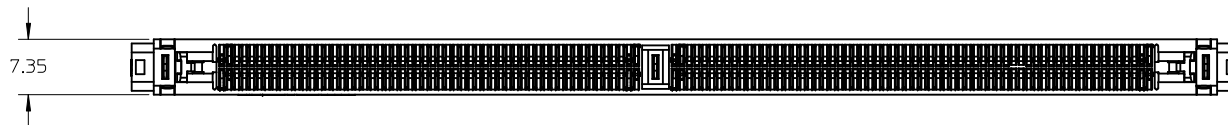
10. DIMM MODULE SEATING PLANE FROM TOP OF PCB.

11. CONTACT MOLEX FOR AVAILABILITY OF PRODUCT OPTIONS.

12. PART NUMBER SHALL BE MARKED LEGIBLY AS 87978-****



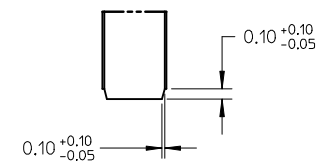
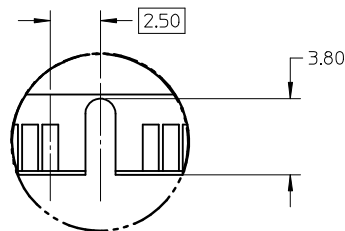
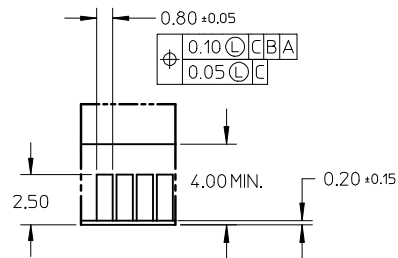
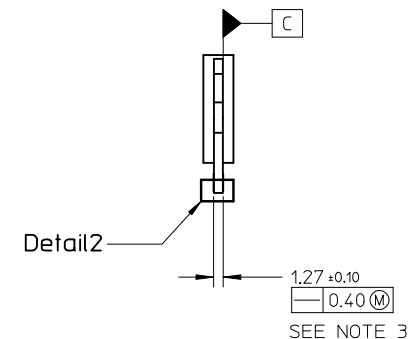
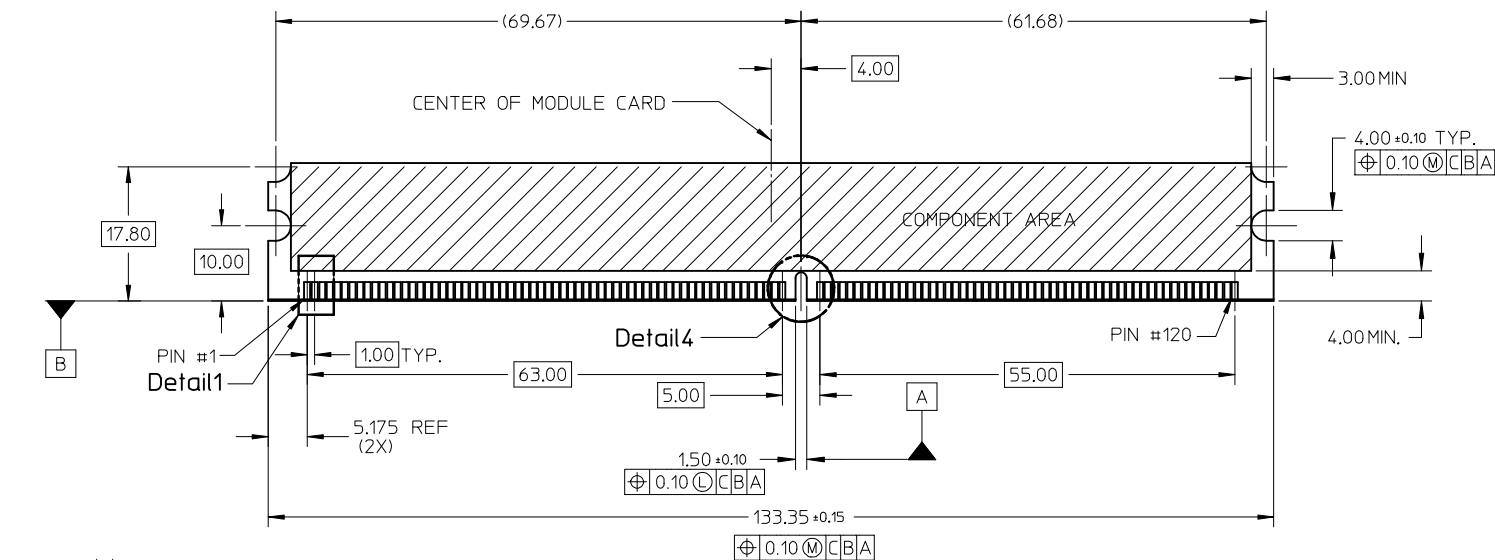
REVISED EC NO: S2008-0235 DRW: CMTE0 CHKD: CCTEH APPR: SHLENI	2007/09/27	DESCRIPTION	QUALITY SYMBOLS ▽=0 ▽=0	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	 THIRD ANGLE PROJECTION	
	2007/09/27					DRAWN BY DATE		TITLE DDR2 DIMM, 1MM PITCH 240 CKTS, FORK-LOCK VERS VLP LATCH			
	2007/09/28					CHECKED BY DATE					
					APPROVED BY DATE						
					MATERIAL NO.		DOCUMENT NO.				SHEET NO.
B	REV			DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		SEE TABLE		SD-87978-001		1 OF 5	
THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION											



REVISED EC NO: S2008-0235 2007/09/27 DRWN:CMTEO CHKD:CCTEH 2007/09/27 APPR:SHENI 2007/09/28	DESCRIPTION REV	QUALITY SYMBOLS ▽=0 ◻=0	GENERAL TOLERANCES (UNLESS SPECIFIED)			DIMENSION STYLE		SCALE NTS	DESIGN UNITS METRIC	 THIRD ANGLE PROJECTION	TITLE DDR2 DIMM, 1MM PITCH 240 CKTS, FORK-LOCK VERS VLP LATCH  MOLEX INCORPORATED SD-87978-001
						MM ONLY					
			4 PLACES	± ---	± ---	CHECKED BY SRRAMESH	DATE 2005/08/30				
				3 PLACES	± ---			± ---			
				2 PLACES	± 0.20			± ---			
				1 PLACE	± ---			± ---			
			ANGULAR ± 5 °			APPROVED BY GGLEE	DATE 2005/08/30	DOCUMENT NO.	SHEET NO. 2 OF 5		
			DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS			MATERIAL NO.					
						SEE TABLE					
						THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION					

10 9 8 7 6 5 4 3 2 1

RECOMMENDED MODULE LAYOUT



KEY LOCATION	VOLTAGE
CENTER	1.8V

NOTES:

1. MODULE CARD USED FOR TESTS ARE WITH 0.10MM CHAMFER.

REVISED EC NO: S2008-0235 DRWN:CMTEO CHKD:CCTEH APPR:SHLENI	2007/09/27 2007/09/27 2007/09/28	DESCRIPTION	QUALITY SYMBOLS ▽=0 ▽C=0	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	 THIRD ANGLE PROJECTION
B	REV			DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		SEE TABLE		SD-87978-001		SHEET NO. 3 OF 5
				THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION						

PART NO.	KEY POS.	DIM 'P'	RECOMMENDED PCB THICKNESS	PLATING OPTION	LATCH COLOR
87978-0021	CENTER (1.8V)	2.67	1.57	0.38uM / 15 uIN MIN. GOLD ON CONTACT, 2.54uM/ 100uIN MIN. TIN ON SOLDER TAILS, 1.27uM/ 50uIN MIN. NICKEL UNDERPLATE	BLACK
87978-1021	CENTER (1.8V)	3.18	2.36		
87978-0031	CENTER (1.8V)	2.67	1.57	0.03uM / 1 uIN MIN. GOLD ON CONTACT, 2.54uM/ 100uIN MIN. TIN ON SOLDER TAILS, 1.27uM/ 50uIN MIN. NICKEL UNDERPLATE	
87978-1031	CENTER (1.8V)	3.18	2.36		
87978-0051	CENTER (1.8V)	2.67	1.57	0.76uM / 30 uIN MIN. GOLD ON CONTACT, 2.54uM/ 100uIN MIN. TIN ON SOLDER TAILS, 1.27uM/ 50uIN MIN. NICKEL UNDERPLATE	
87978-1051	CENTER (1.8V)	3.18	2.36		

REVISED

EC NO: S2008-0235

DRWN:CMTE0

CHKD:CTEH

APPR:SHLENI

B

2007/09/27

2007/09/27

2007/09/28

DESCRIPTION

REV

QUALITY SYMBOLS

=0

=0

GENERAL TOLERANCES (UNLESS SPECIFIED)

	mm	INCH
4 PLACES	± ---	± ---
3 PLACES	± ---	± ---
2 PLACES	± 0.20	± ---
1 PLACE	± ---	± ---

ANGULAR ± 5 °

DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS

DIMENSION STYLE

MM ONLY

DRAWN BY	DATE
CTEH	2005/01/03
CHECKED BY	DATE
SRAMESH	2005/08/30
APPROVED BY	DATE
GGLEE	2005/08/30

MATERIAL NO.

SEE TABLE

SIZE

A3

SCALE

NTS

DESIGN UNITS

METRIC

THIRD ANGLE PROJECTION

TITLE

DDR2 DIMM, 1MM PITCH
240 CKTS, FORK-LOCK VERS
VLP LATCH

MOLEX INCORPORATED

DOCUMENT NO.

SD-87978-001

SHEET NO.

5 OF 5

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