

Enhanced Test Condition

PSE Conformance Test Suite

February 12 2025 3:41 PM

Port Count..... 1

Loop Count..... 1

PSE Tested: **Unspecified Type-2 30W**



802.3at Conformance Report

version 5.3.14

report

30 Watt PHY

version

Test Mode:

5.3.08

Sifos Interop Index*: 100%

Error Log: C:/Users/Public/Sifos/PSA3000/Results/192.168.221.105/psa_error_log.txt

Chassis ID: 192.168.221.105

PSA-3000 Ports

TestLoop: 1

	1-1	UNITS	Min	Max	Average	Low Limit	P/F	High Limit	P/F
Test: det_v									
Not_Measured		N/A	0	0					
Test: det_i									
Init_Current_Isc=	1.13	mA	1.13	1.13	1.13	0	Pass	5	Pass
Det_Current_Isc=	1.06	mA	1.06	1.06	1.06	0	Pass	5	Pass
Test: det_range									
Rgood_Max=	29	Kohm	29	29	29	26	Pass	32	Pass
Rgood_Min=	17	Kohm	17	17	17	16	Pass	19	Pass
Rmid_det=	29	Kohm	29	29	29	26	Pass	33	Pass
Cgood_Max=	0.1	uF	0.1	0.1	0.1	0	Pass	10	Pass
Rbad_Cbad_Stat=	0	****	0	0	0	0	Pass	0	Pass
4Pair_Pwr_?=	0	****	0	0	0	0	Pass	0	Pass
Test: det_time									
Backoff_Time_Tdbo=	727	msec	727	727	727	-1	Pass	1500	Pass
Eff_Backoff_Tdbo_eff=	727	msec	727	727	727	-1	Pass	1500	Pass
Backoff_Type=	0	****	0	0	0	0	Pass	0	Pass
Detection_Time_Tdet=	283	msec	283	283	283	5	Pass	500	Pass
Total_Det_Time=	293	msec	293	293	293	5	Pass	1000	Pass
Test: det_rsource									
Not_Measured		N/A	0	0					
Test: class_v									
Vclass_Max=	18.5	V	18.5	18.5	18.5	15.5	Pass	20.5	Pass
Vclass_Min=	18.1	V	18.1	18.1	18.1	15.5	Pass	20.5	Pass
Mark_Voltage_Vmark=	8.9	V	8.9	8.9	8.9	7	Pass	10	Pass
Mark_Voltage_Min=	8.6	V	8.6	8.6	8.6	7	Pass	10	Pass

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Test: class_time									
Class_0_Count=	1	****	1	1	1	1	Pass	2	Pass
Class_Time_Tpdc=	9.5	msec	9.5	9.5	9.5	6	Pass	75	Pass
Class_4_Count=	2	****	2	2	2	2	Pass	2	Pass
Event1_Tcle1=	12.9	msec	12.9	12.9	12.9	6	Pass	30	Pass
Event2_Tcle2=	16.4	msec	16.4	16.4	16.4	6	Pass	30	Pass
Mark_Tme1=	9.7	msec	9.7	9.7	9.7	6	Pass	12	Pass
Mark_Tme2=	9.7	msec	9.7	9.7	9.7	6	Pass	376	Pass
Mark_Tme1_LowI=	9.7	msec	9.7	9.7	9.7	6	Pass	12	Pass
Test: class_err									
Class_lim=	72	mA	72	72	72	51	Pass	100	Pass
Pwr_Cl_lim=	0	****	0	0	0	0	Pass	0	Pass
Pwr_Cl_55=	0	****	0	0	0	0	Pass	0	Pass
Mark_lim=	6	mA	6	6	6	5	Pass	100	Pass
Pwr_Cl_Uneven=	0	****	0	0	0	0	Pass	0	Pass
Treset=	82	msec	82	82	82	15	Pass	10000	Pass
Test: pwrup_time									
Power-On_Trise_c0=	55	usec	55	55	55	15	Pass	50000	Pass
Power-On_Tpon_c0=	9.8	msec	9.8	9.8	9.8	0	Pass	400	Pass
Power-On_Trise_c4=	63	usec	63	63	63	15	Pass	50000	Pass
Power-On_Tpon_c4=	48.8	msec	48.8	48.8	48.8	0	Pass	400	Pass
Test: pwrup_inrush									
Init_Iinrush=	428.25	mA	428.25	428.25	428.3	400	Pass	450	Pass
Max_Iinrush_c0=	425.75	mA	425.75	425.75	425.8	400	Pass	450	Pass
Max_Iinrush_c4=	425.5	mA	425.5	425.5	425.5	400	Pass	450	Pass
Min_Iinrush=	424.75	mA	424.75	424.75	424.8	400	Pass	450	Pass
Tinrush=	60.7	msec	60.7	60.7	60.7	50	Pass	75	Pass
Inrush_45m=	55.1	V	55.1	55.1	55.1	50	Pass	57	Pass
Inrush_Voltage=	30.9	V	30.9	30.9	30.9	30	Pass	57	Pass
Inrush_Strategy_c0=	1	****	1	1	1	0	Pass	1	Pass
Inrush_Strategy_c4=	0	****	0	0	0	0	Pass	1	Pass
Test: pwrn_v									
Vport_min=	54.3	V	54.3	54.3	54.3	50	Pass	57	Pass
Vport_max=	55.3	V	55.3	55.3	55.3	50	Pass	57	Pass
Vport_ripple=	61	mVpp	61	61	61	0	Pass	500	Pass

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Vport_noise=	62	mVpp	62	62	62	0	Pass	200	Pass
Vtrans_min=	54.2	V	54.2	54.2	54.2	50	Pass	57	Pass
Vtrans_max=	55.4	V	55.4	55.4	55.4	50	Pass	57	Pass
Test: pwron_pwrcap									
Pcon_c0=	18.5	W	18.5	18.5	18.5	14.2	Pass	22.7	Pass
Icon_%_c0=	134.6	%	134.6	134.6	134.6	100	Pass	9999	Pass
Pcon_c1=	18.5	W	18.5	18.5	18.5	3.9	Pass	22.7	Pass
Icon_%_c1=	474.4	%	474.4	474.4	474.4	100	Pass	9999	Pass
Pcon_c2=	18.5	W	18.5	18.5	18.5	6.8	Pass	22.7	Pass
Icon_%_c2=	277.3	%	277.3	277.3	277.3	100	Pass	9999	Pass
Pcon_c3=	18.5	W	18.5	18.5	18.5	14.2	Pass	22.7	Pass
Icon_%_c3=	134.6	%	134.6	134.6	134.6	100	Pass	9999	Pass
Pcon_c4=	34	W	34	34	34	28.7	Pass	38.9	Pass
Icon_%_c4=	117.1	%	117.1	117.1	117.1	100	Pass	9999	Pass
Type-2_Enable=	1	****	1	1	1	1	Pass	1	Pass
Test: pwron_maxi									
Ilim_Peak_1=	272.3	mA	272.3	272.3	272.3	0	Pass	1750	Pass
Ilim_Min_1=	403.6	mA	403.6	403.6	403.6	400	Pass	1750	Pass
Tlim_1=	61.7	msec	61.7	61.7	61.7	10	Pass	9999	Pass
Vlim_1=	54.5	V	54.5	54.5	54.5	50	Pass	57	Pass
Ilim_Max_1=	424.3	mA	424.3	424.3	424.3	400	Pass	1750	Pass
Ktran_lo_1=	108.8	%	108.8	108.8	108.8	92.4	Pass	115	Pass
Ilim_Peak_2=	338.5	mA	338.5	338.5	338.5	0	Pass	1750	Pass
Ilim_Min_2=	687	mA	687	687	687	683	Pass	1750	Pass
Tlim_2=	61.3	msec	61.3	61.3	61.3	10	Pass	75	Pass
Vlim_2=	54	V	54	54	54	50	Pass	57	Pass
Ilim_Max_2=	824.5	mA	824.5	824.5	824.5	683	Pass	1750	Pass
Ktran_lo_2=	107.7	%	107.7	107.7	107.7	92.4	Pass	115	Pass
Test: pwron_overld									
%Ipeak_1=	125	%	125	125	125	100	Pass	125	Pass
Vport_Ipeak_1=	54.6	V	54.6	54.6	54.6	50	Pass	57	Pass
Vport_5%DC_1=	54.6	V	54.6	54.6	54.6	50	Pass	57	Pass
%Ipeak_2=	125	%	125	125	125	100	Pass	125	Pass
Vport_Ipeak_2=	54.1	V	54.1	54.1	54.1	50	Pass	57	Pass
Vport_5%DC_2=	54.1	V	54.1	54.1	54.1	50	Pass	57	Pass

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Test: mps_dc_valid									
Min_Valid_Time_Tmps=	15	msec	15	15	15	1	Pass	60	Pass
Duty_Cycle_tol=	1	****	1	1	1	1	Pass	1	Pass
Test: mps_dc_pwrnd									
Min_Valid_I_hold=	5	mA	5	5	5	5	Pass	10	Pass
Time-to-Shutdown_Tmpdo=	358	msec	358	358	358	300	Pass	400	Pass
Test: pwrnd_overld									
Icut_1=	377.5	mA	377.5	377.5	377.5	-1	Pass	1750	Pass
Tcut_1=	61.4	msec	61.4	61.4	61.4	50	Pass	9999	Pass
Isoft_1=	-1	mA	-1	-1	-1	-1	Pass	683	Pass
Tsoft_1=	-1	msec	-1	-1	-1	-1	Pass	2000	Pass
Icut_2=	640.5	mA	640.5	640.5	640.5	-1	Pass	1750	Pass
Tcut_2=	61.4	msec	61.4	61.4	61.4	10	Pass	9999	Pass
Isoft_2=	-1	mA	-1	-1	-1	-1	Pass	683	Pass
Tsoft_2=	-1	msec	-1	-1	-1	-1	Pass	2000	Pass
Test: pwrnd_time									
Turn-Off_Time_Toff=	20.4	msec	20.4	20.4	20.4	0	Pass	500	Pass
Output_Cap_Cout=	0.0399	uF	0.0399	0.0399	0.04	-1	Pass	0.52	Pass
Output_Load_Rp=	400.4	Kohm	400.4	400.4	400	45	Pass	50000	Pass
Test: pwrnd_v									
Avg_Idle_Voff=	0.1	V	0.1	0.1	0.1	0	Pass	2.8	Pass
Error_Delay_Ted=	1367.2	msec	1367.2	1367.2	1367	750	Pass	10000	Pass
Peak_Error_Delay_Ved=	1.3	V	1.3	1.3	1.3	0	Pass	20.5	Pass
Test Port Model Number:	3202								
Test Port Hardware Version:	11								
Test Port Firmware Version:	4.19 lc19								