

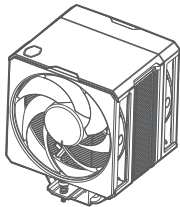


HYPER 612 APEX

MANUAL

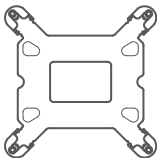
01.PACKAGE CONTENT

A



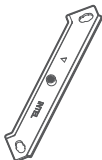
x1

B



x1

C



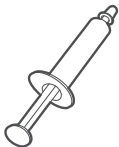
x2

D



x2

E



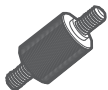
x1

F



x4

G



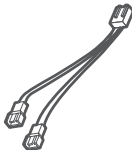
x4

H



x4

I



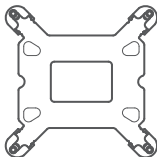
x1

02. INTEL & AMD

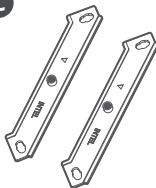
INTEL LGA 1851/1700/1200/115X: B,C,G,H

AMD AM5/AM4: F,D,H

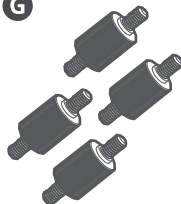
B



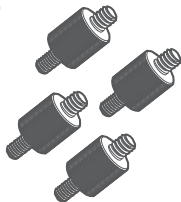
C



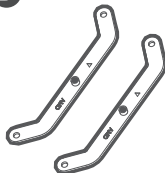
G



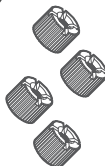
F



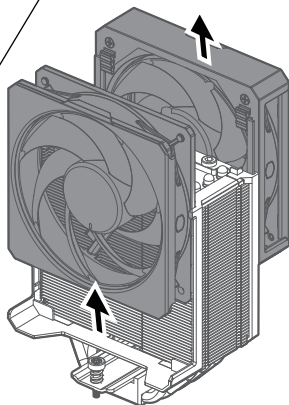
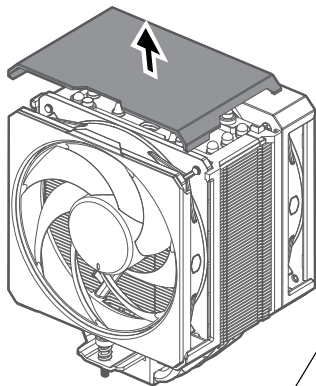
D



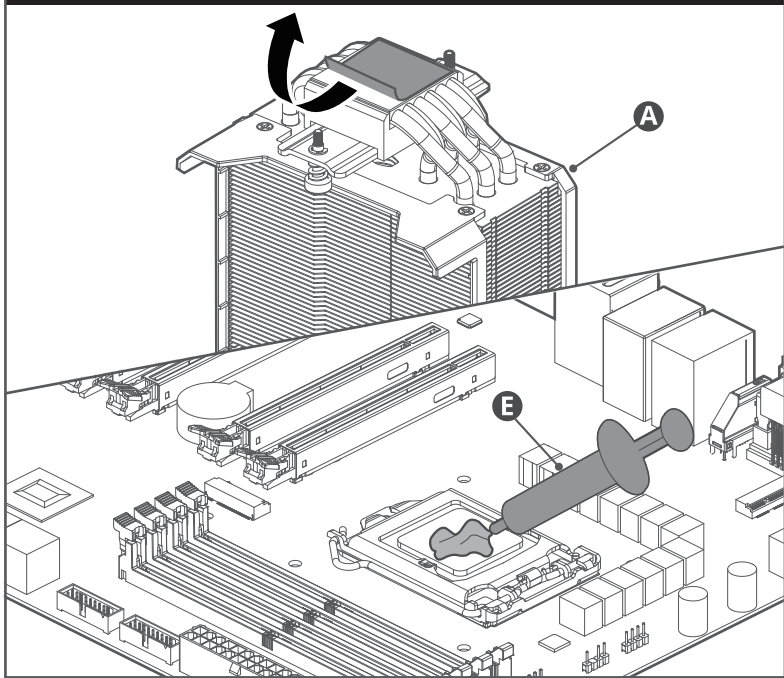
H



03. INTEL & AMD

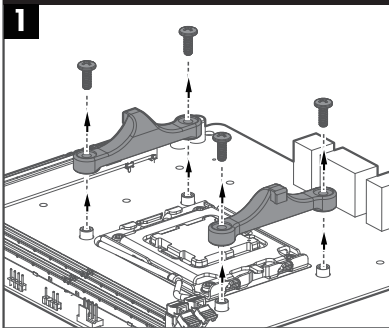


04. INTEL & AMD

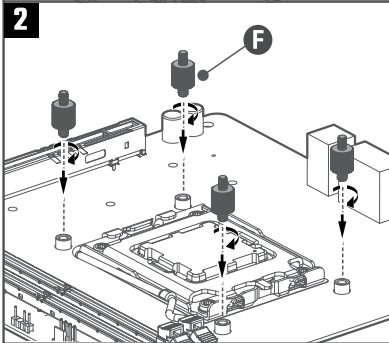


05. AMD - AM5/AM4

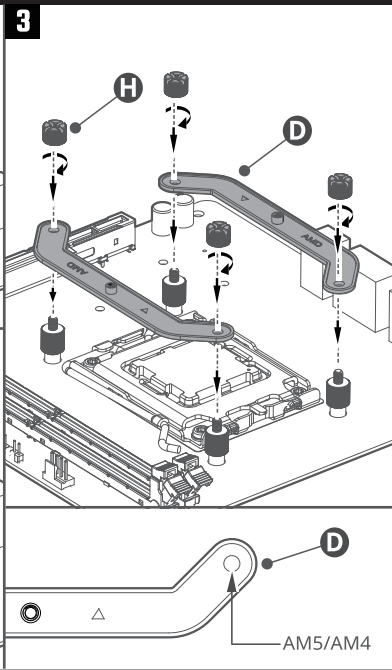
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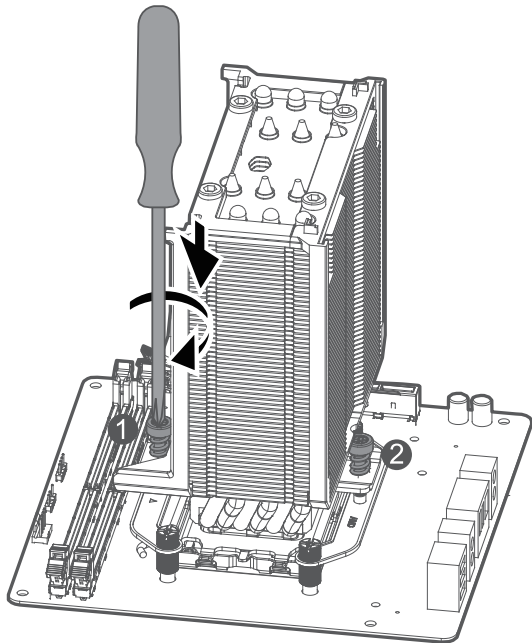
2



3

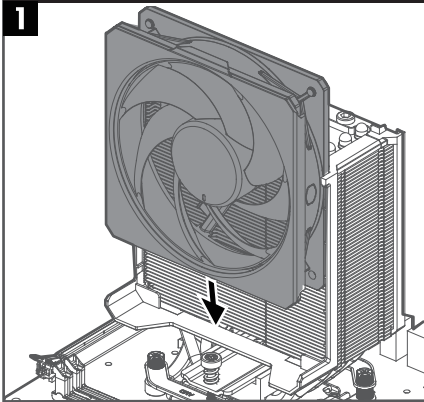


06. AMD - AM5/AM4

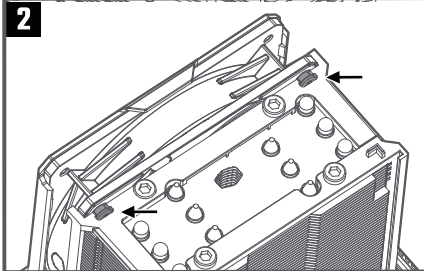


07. AMD - AM5/AM4

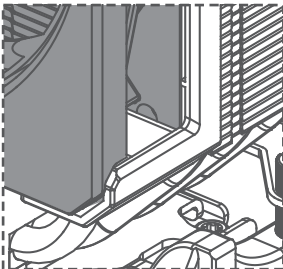
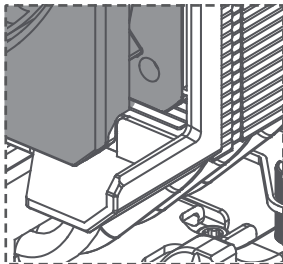
1



2

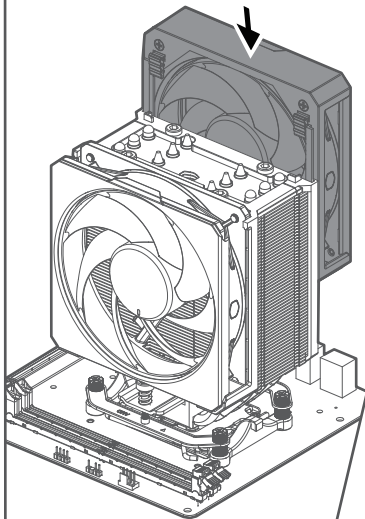


3

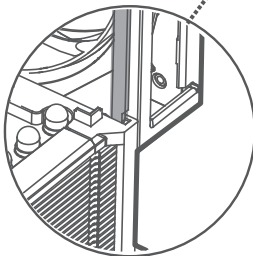
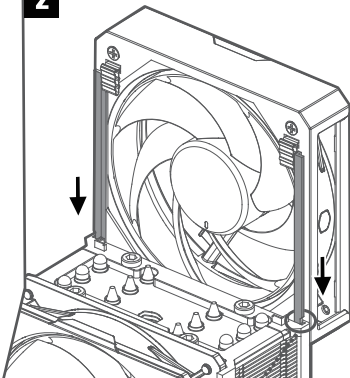


08. AMD-AM5/AM4

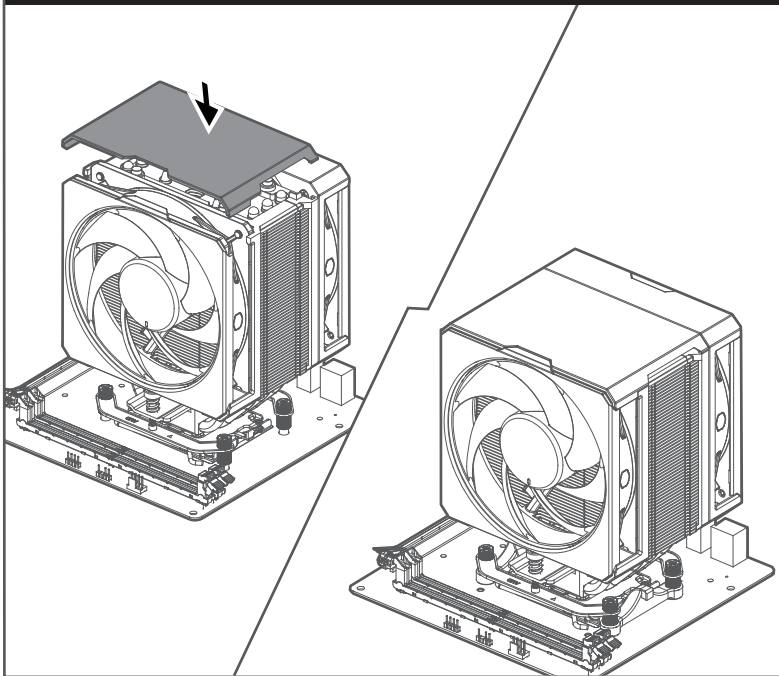
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2



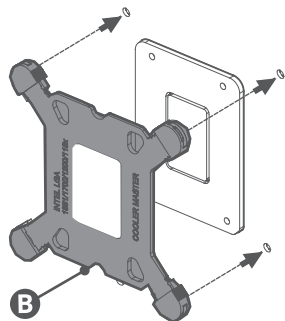
09. AMD-AM5/AM4



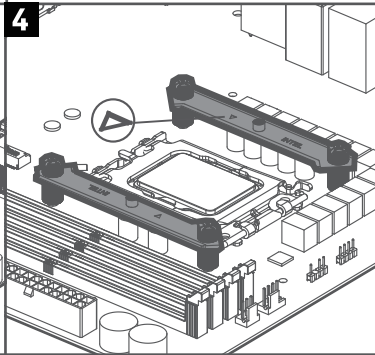
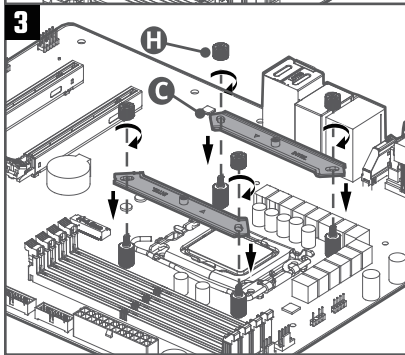
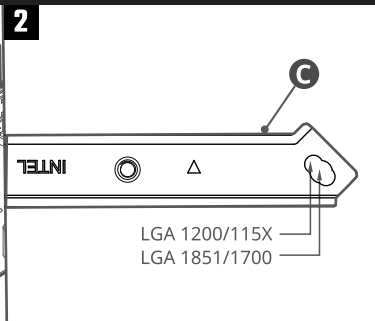
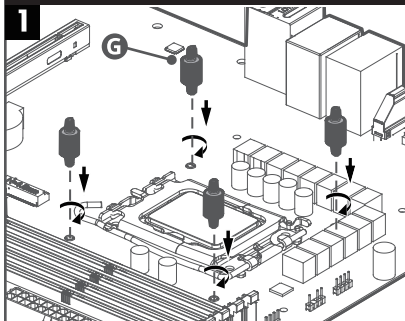
10. INTEL - LGA 1851/1700/1200/115X

LGA 1851/1700

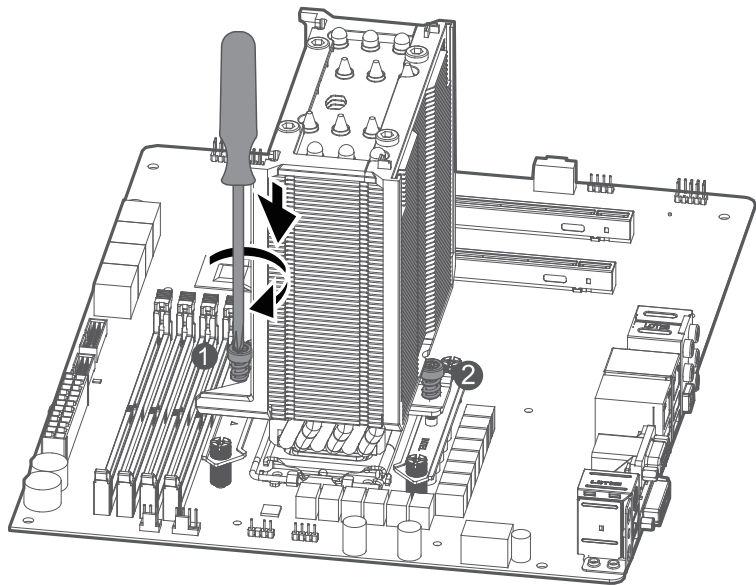
LGA 115X/1200



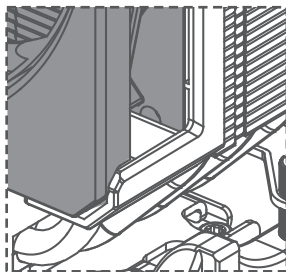
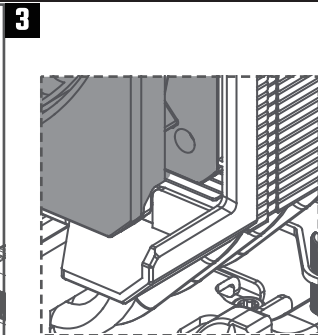
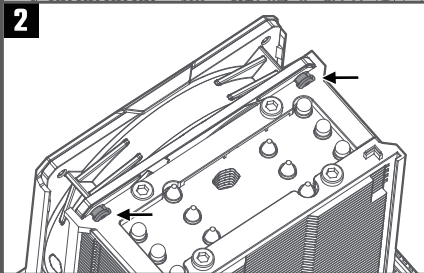
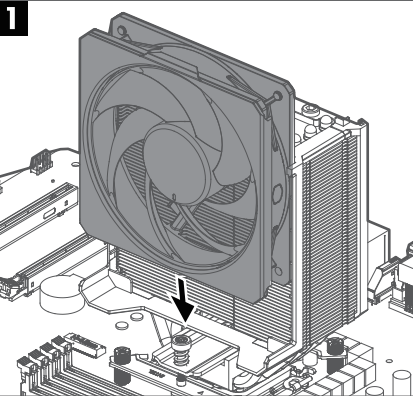
11. INTEL - LGA 1851/1700/1200/115X



12. INTEL - LGA 1851/1700/1200/115X

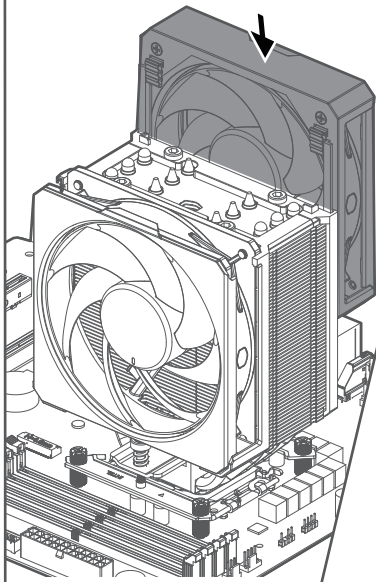


13. INTEL - LGA 1851/1700/1200/115X

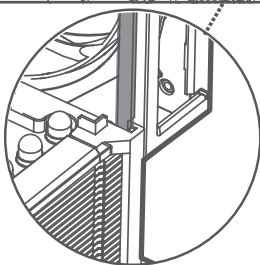
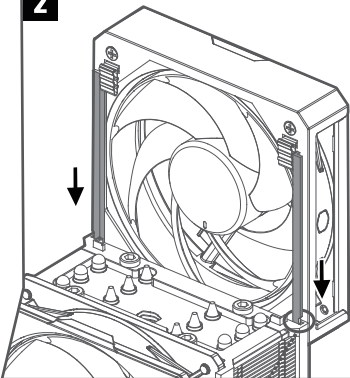


14. INTEL - LGA 1851/1700/1200/115X

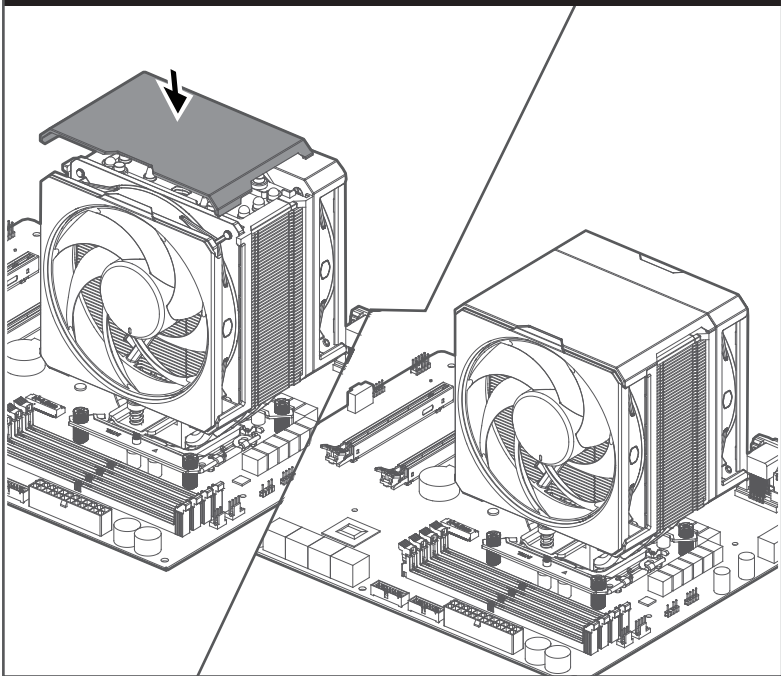
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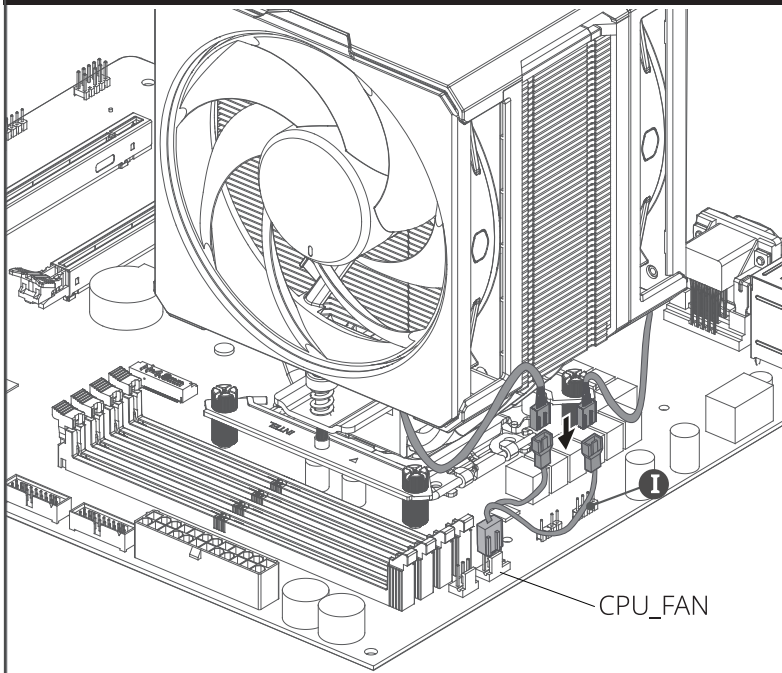
2



15. INTEL - LGA 1851/1700/1200/115X



16. PWM CONNECTION



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